

SC20100S/B Pinout

100S	100B	I/O	Function	100S	100B	I/O	Function	100S	100B	I/O	Function	100S	100B	I/O	Function
1	A3	PE2	QSPI IO2	26	C2		VSS	51	K8	PB12	CAN2 RX	76	A9	PA14	
2	B3	PE3	LDR	27	D2		VDD	52	J8	PB13	CAN2 TX	77	A8	PA15	UART4 RTS
3	C3	PE4		28	G3	PA4	ADC12.18	53	H10		UART5 RX	78	B9	PC10	SDMMC1 D2
4	D3	PE5	PWM15.1	29	H3	PA5	SPI6 SCK	54	G10		UART5 TX	79	B8	PC11	SDMMC1 D3
5	E3	PE6	PWM15.2	30	J3	PA6	SPI6 MISO	55	K9	PD8	USBH N	80	C8	PC12	SDMMC1 CK
6	B2	PC13	VBAT	31	K3	PA7	SPI6 MOSI	56	J9	PD9	USBH P	81	D8	PD0	UART4 RX
7	A2	PC15	TAMPER	32	G4	PC4		57	H9	PD10	UART3 TX	82	E8	PD1	UART4 TX
8	A1	PC15	OSC32 IN	33	H4	PC5		58	G9	PD11	UART3 RX	83	B7	PD2	CAN1 RX
9	B1	PC14	OSC32 OUT	34	J4	PB0	UART4 CTS	59	K10	PD12	ETH RXD0	84	C7	PD3	CAN1 TX
10	J1		VSS	35	K4	PB1	PWM3.4	60	J10	PD13	ETH RXD1	85	D7	PD4	SDMMC1 CMD
11	K1		VDD	36	G5	PB2	ADC12.5	61	H8	PD14	QSPI IO0	86	B6	PD5	UART2 CTS
12	C1		OSC IN	37	H5	PE7	QSPI CLK	62	G8	PD15	QSPI IO1	87	C6	PD6	UART2 TX
13	D1		OSC OUT	38	J5	PE8	UART7 RX	63	F10	PC6	QSPI IO3	88	D6	PD7	UART2 RX
14	E1		NRST	39	K5	PE9	UART7 TX	64	E10	PC7	UART6 TX	89	A7	PB3	MOD
15	F1	PC0	ADC123.10	40	G6	PE10	UART7 RTS	65	F9	PC8	UART6 RX	90	A6	PB4	SPI3 SCK
16	F2	PC1	ADC123.11	41	H6	PE11	UART7 CTS	66	E9	PC9	PWM3.1	91	C5	PB5	SPI3 MISO
17	E2	PC2	ADC3.0	42	J6	PE12	PWM1.2	67	D9	PA8	PWM3.2	92	B5	PB6	SPI3 MOSI
18	F3	PC3	ADC3.1	43	K6	PE13	SPI4 SCK	68	C9	PA9	SDMMC1 D0	93	A5	PB7	QSPI NCS
19	G1		VSSA	44	G7	PE14	SPI4 MISO	69	D10	PA10	SDMMC1 D1	94	D5	PE0	PWM4.2
20	F7		VREF+(S)/PDR_ON(B) ¹	45	H7	PE15	SPI4 MOSI	70	C10	PA10	UART1 TX ²	95	B4	PB8	APP
21	H1		VDDA	46	J7	PB10		71	B10	PA10	UART1 RX ²	96	A4	PB9	RESERVED pull to GND
22	G2	PA0	PWM5.1	47	K7	PB11	I2C2 SCL	72	A10	PA13	USBC N	97	D4	PE0	I2C1 SCL
23	H2	PA1	ADC1.16	48	E7		I2C2 SDA	73	F8		USBC P	98	C4	PE1	I2C1 SDA
24	J2	PA2	ETH REF CLK	49	E5		VCAP	74	E4		VSS	99	E6		UART8 RX
25	K2	PA3	ETH MDIO	50	F5		VSS	75	F4		VDD(S)/VDDLDO(B)*	100	F6		UART8 TX
			PWM2.4				VDD								VSS
			ADC12.15												VDD(S)/VDDLDO(B) ¹

¹ Connect to VDD on both S or B packages

² These pins are used when in serial interface mode